

Fig. 1

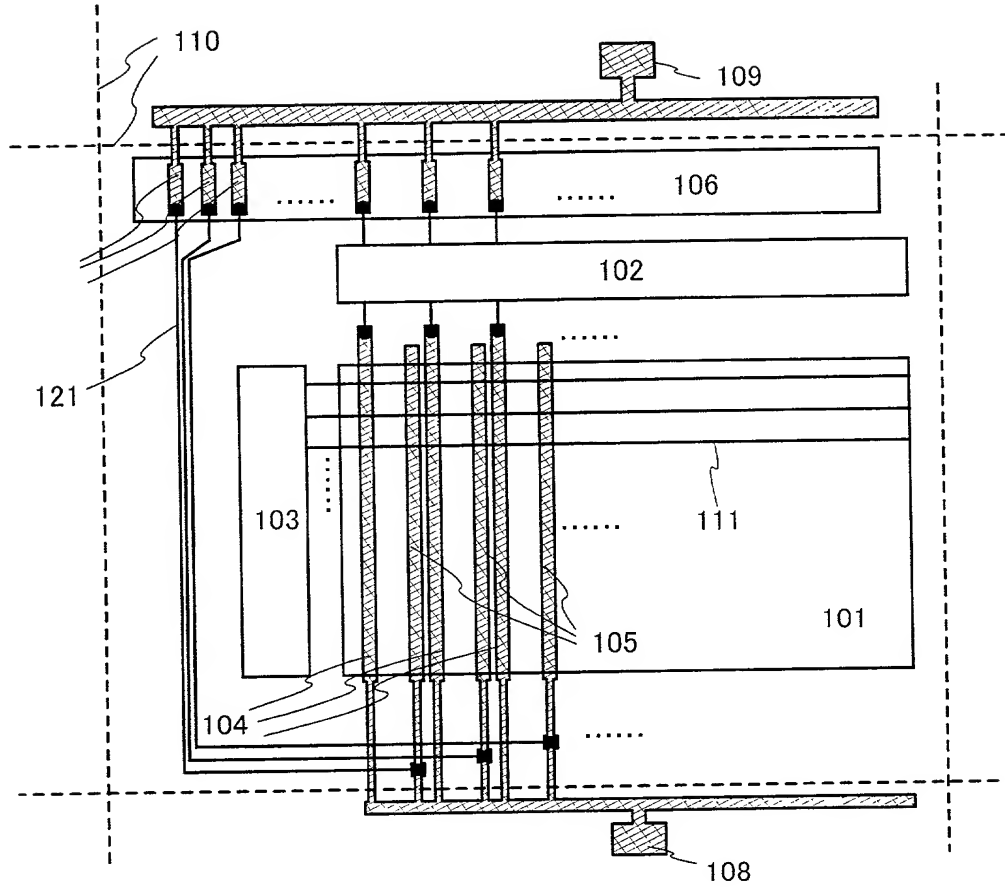


Fig. 2

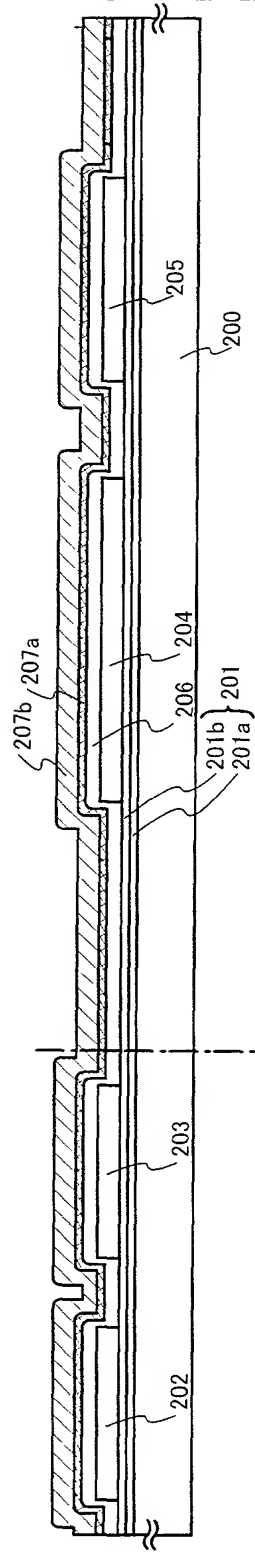


Fig. 3A

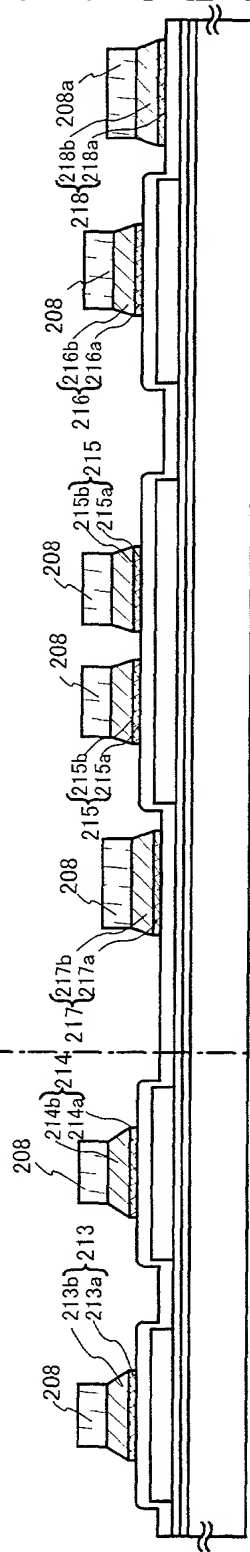


Fig. 3B

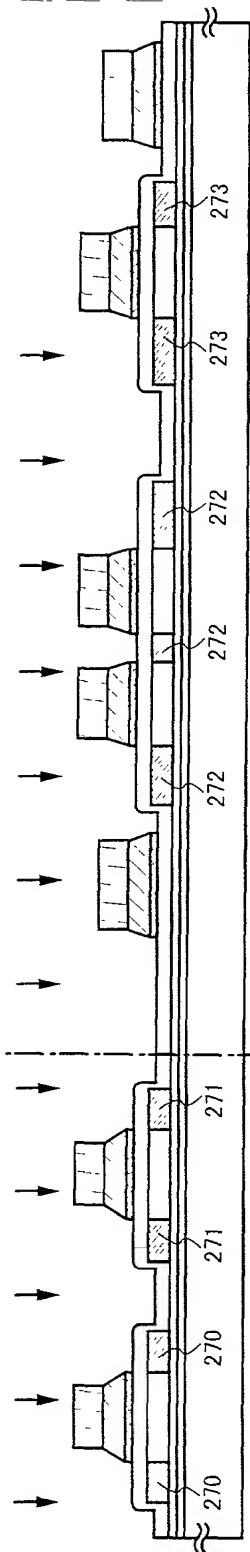


Fig. 3C

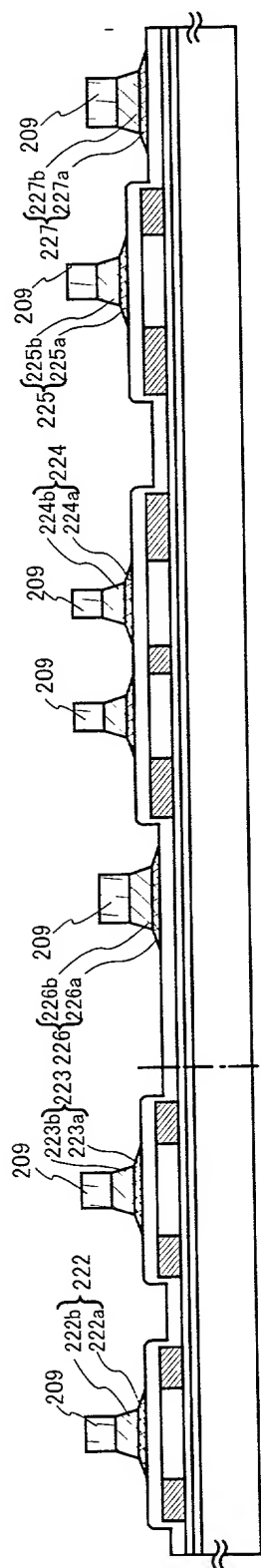


Fig. 4A

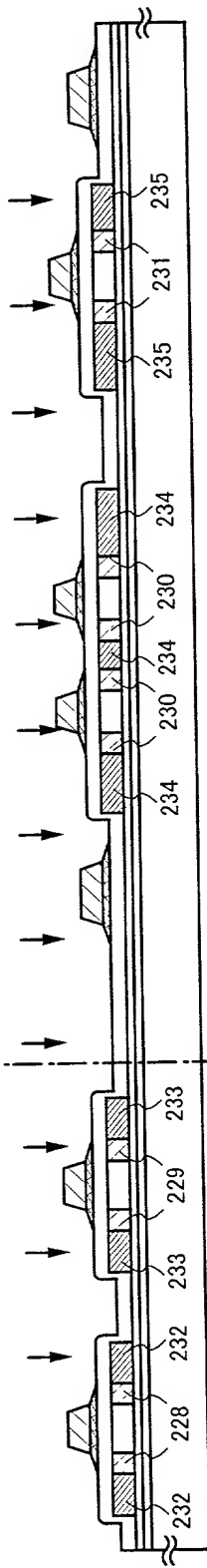


Fig. 4B

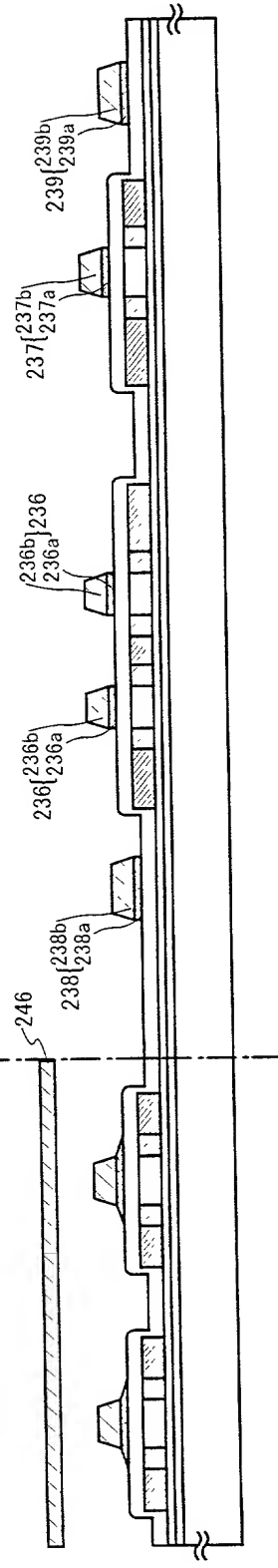


Fig. 4C

Footnote 23222007

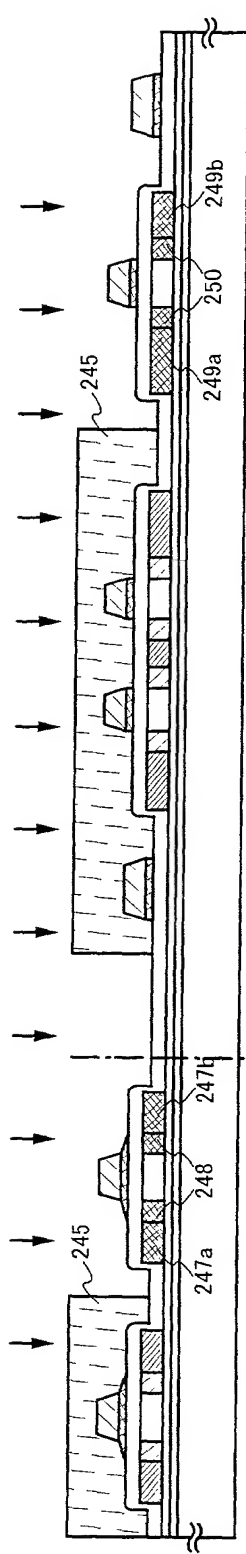


Fig. 5A

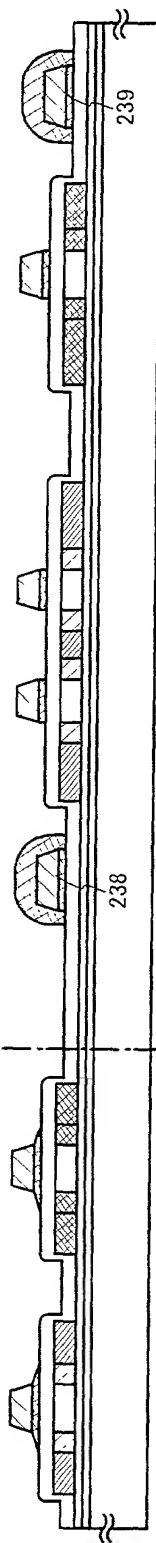


Fig. 5B

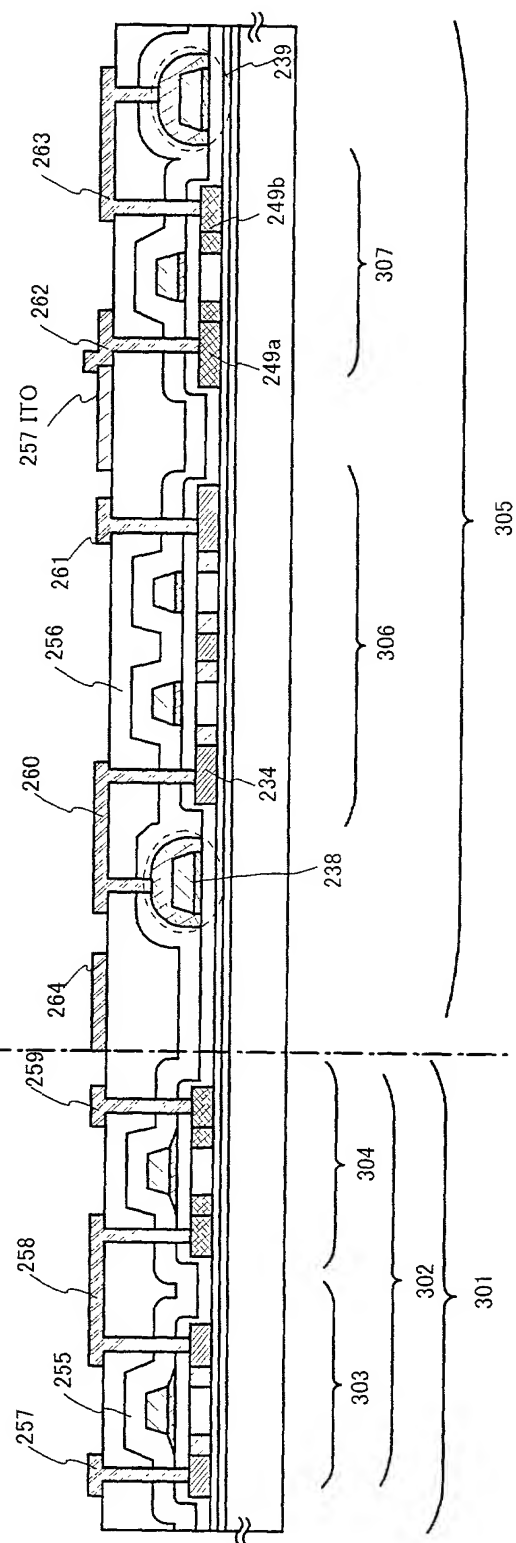


Fig. 5C

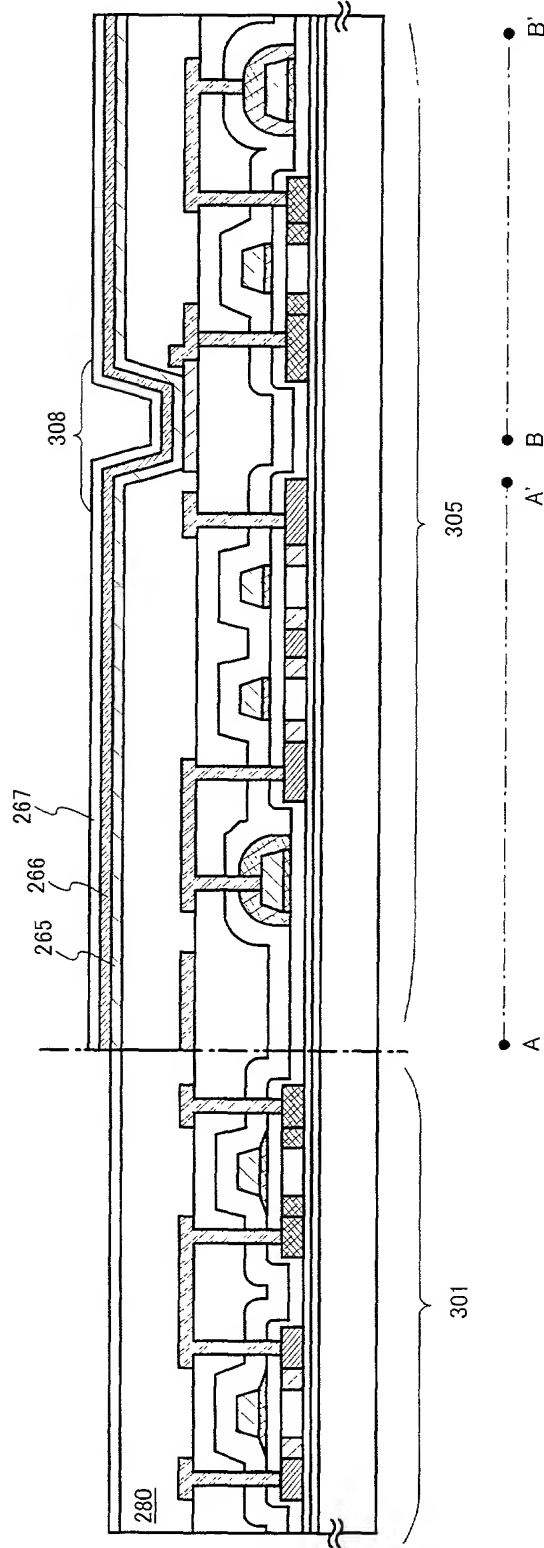


Fig. 6

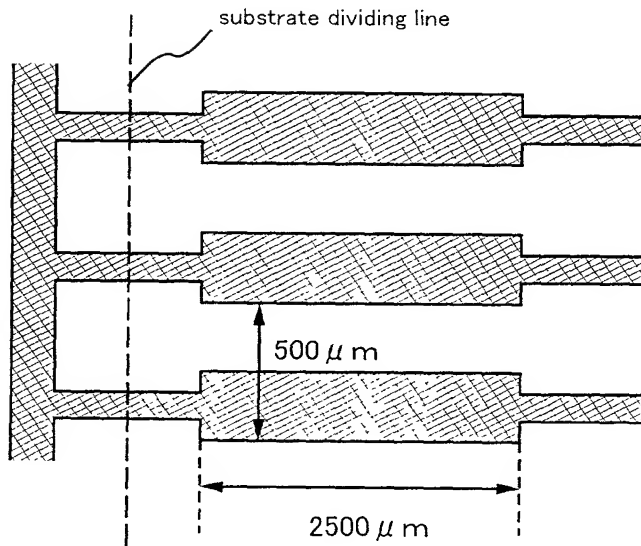


Fig. 7A

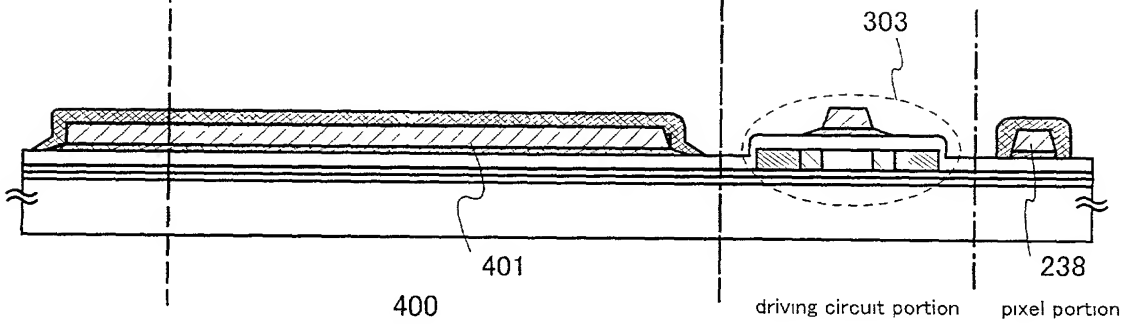


Fig. 7B

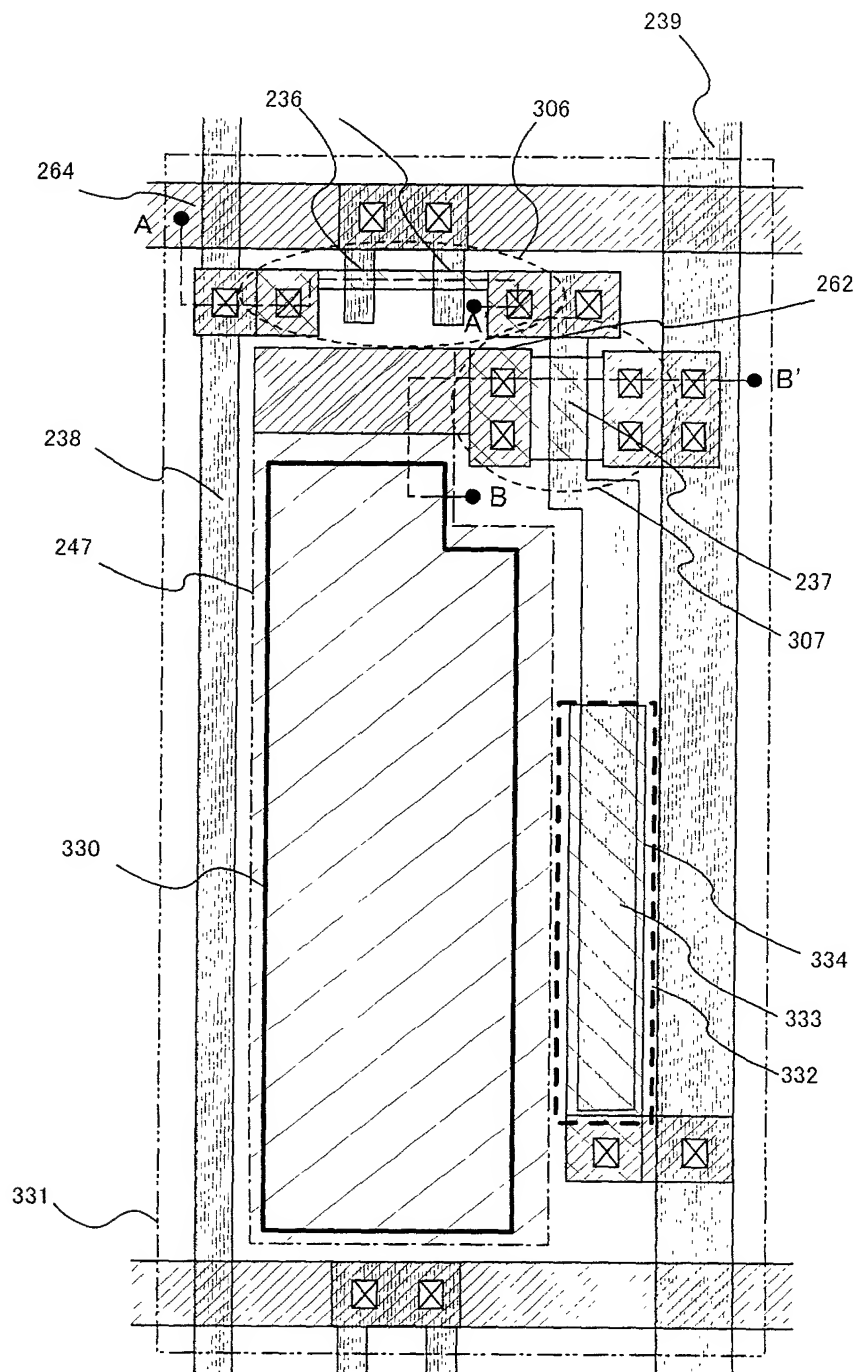


Fig. 8



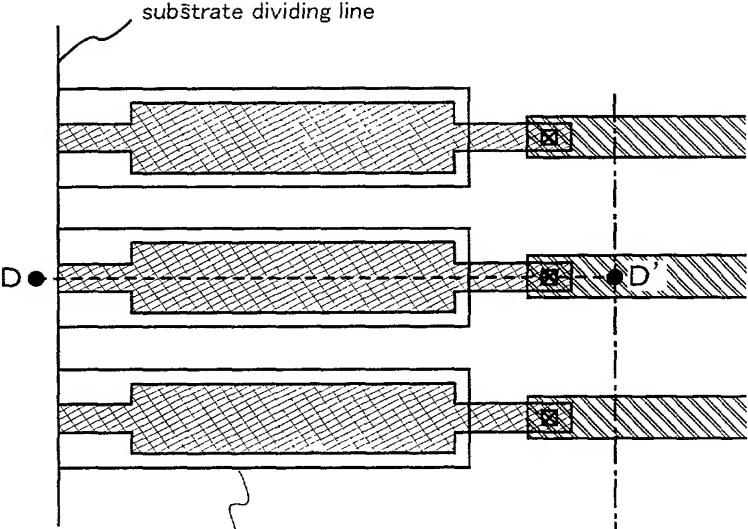


Fig. 9A

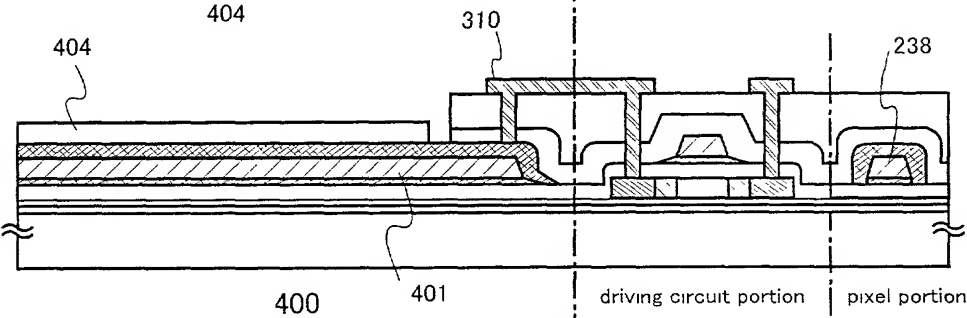


Fig. 9B

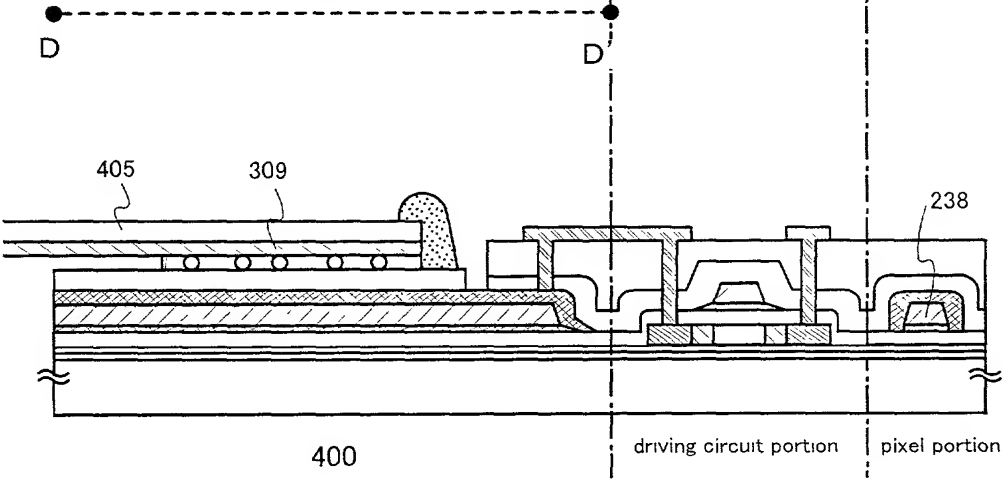


Fig. 9C

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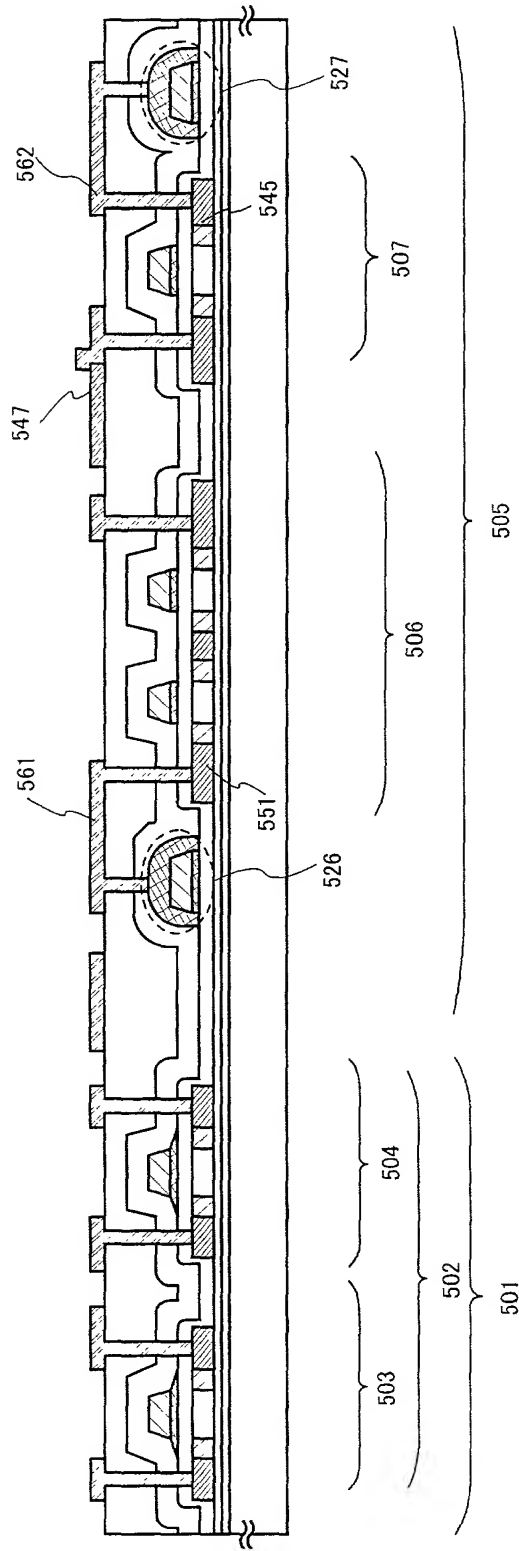


Fig. 10

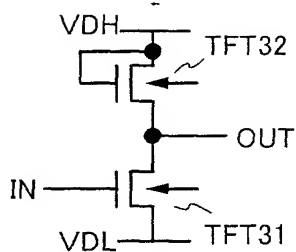


Fig. 11A EEMOS circuit

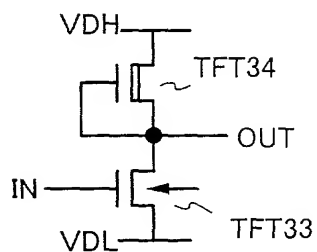


Fig. 11B EDMOS circuit

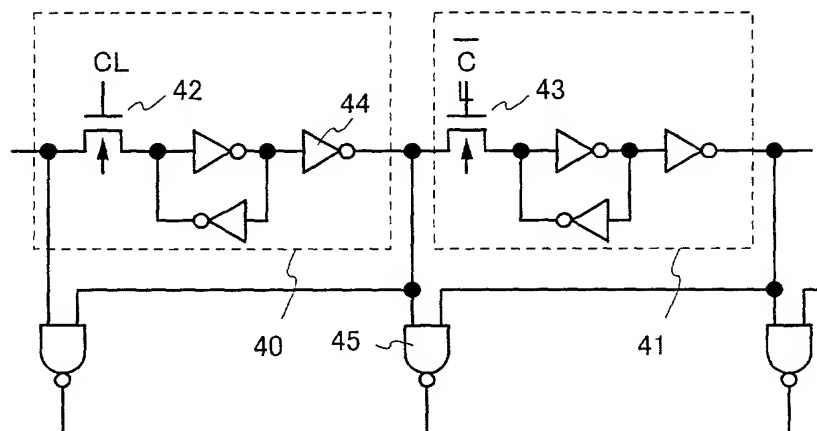


Fig. 12A

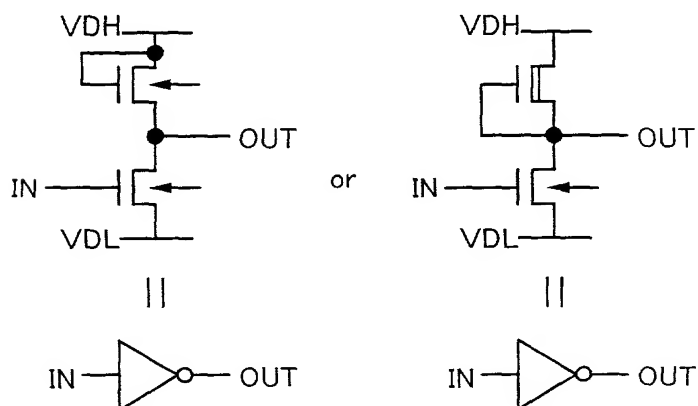


Fig. 12B

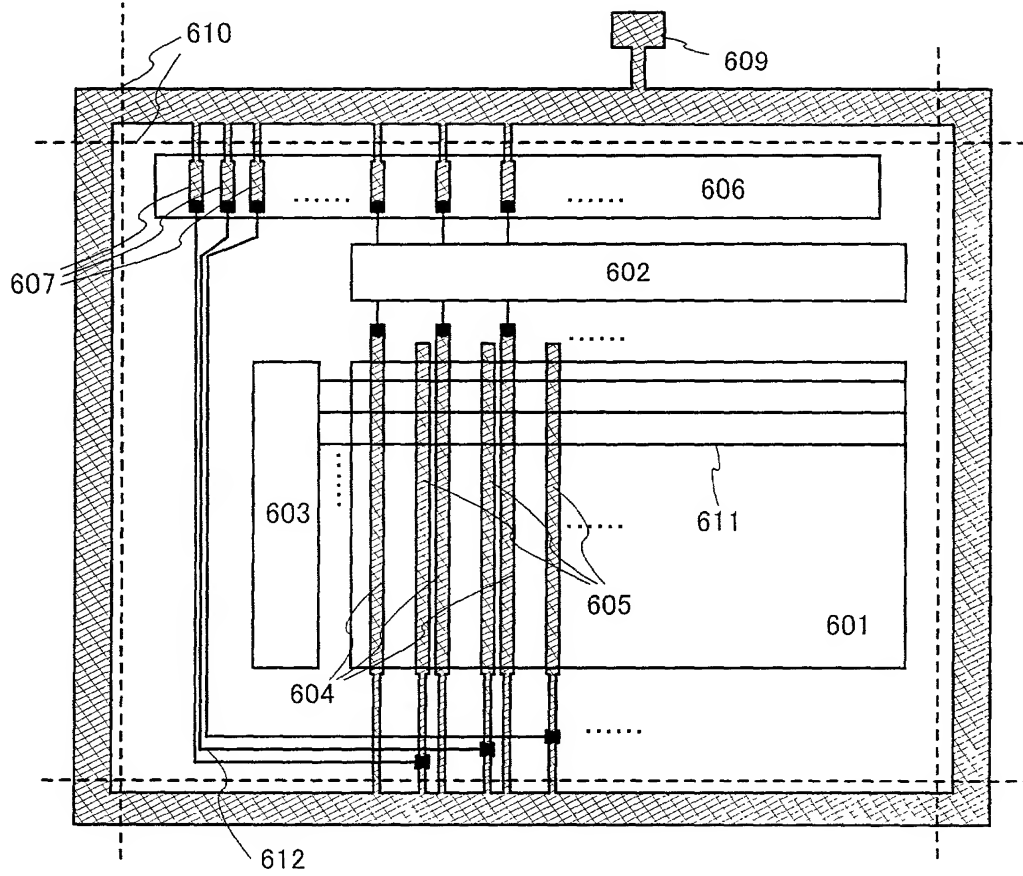


Fig. 13

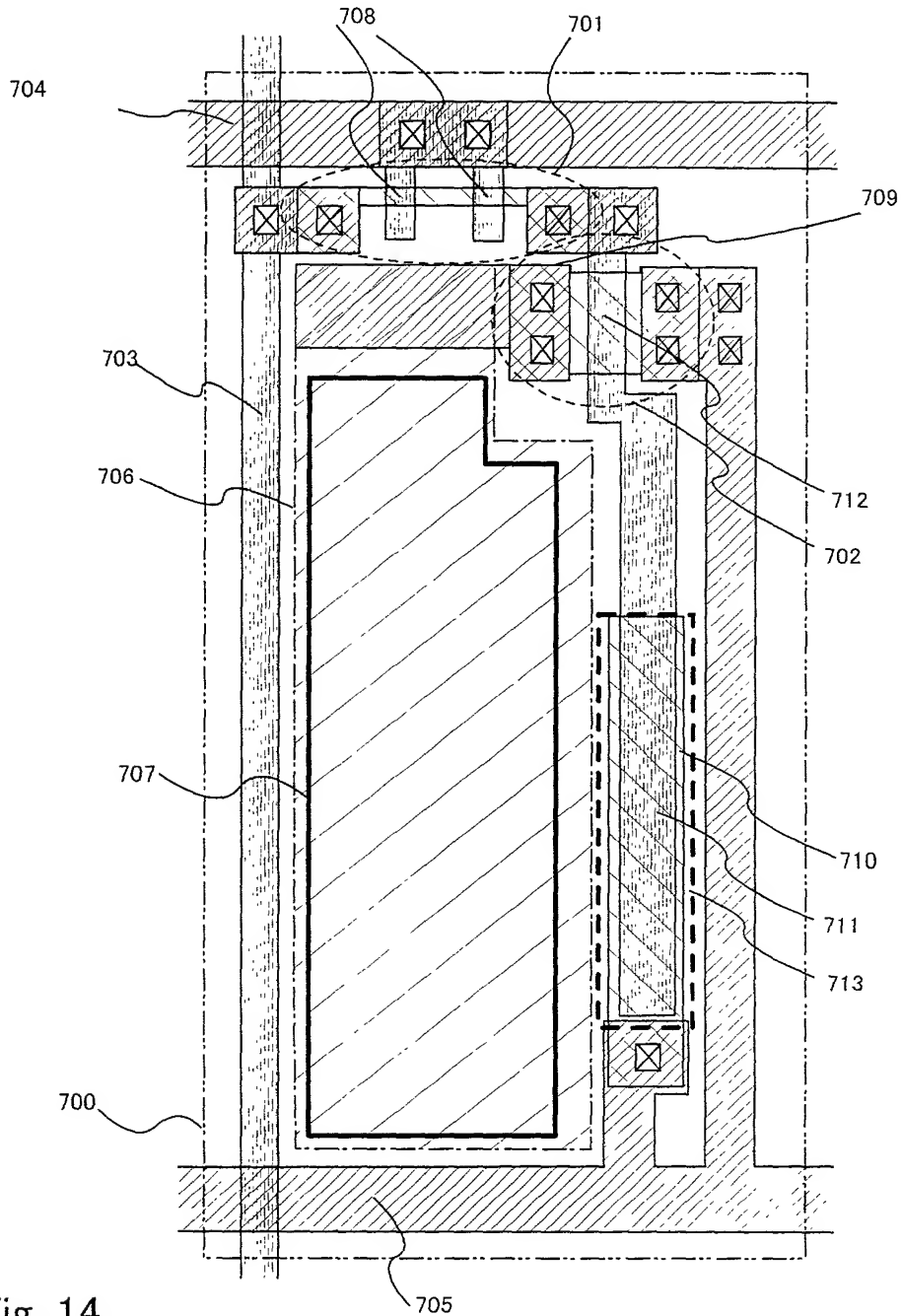


Fig. 14

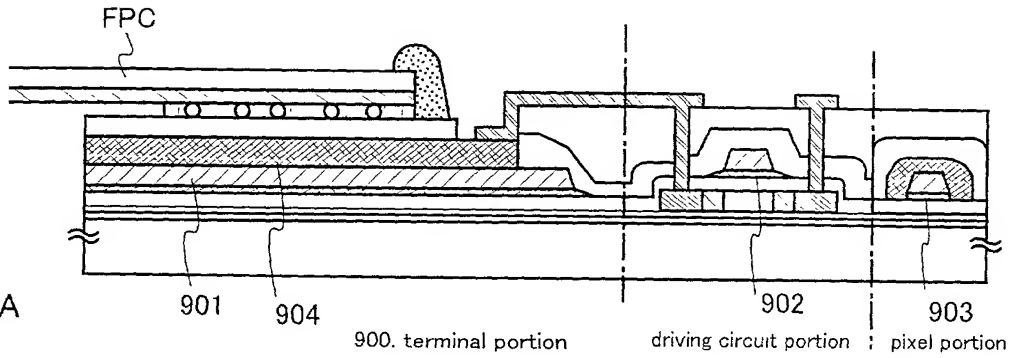


Fig. 15A

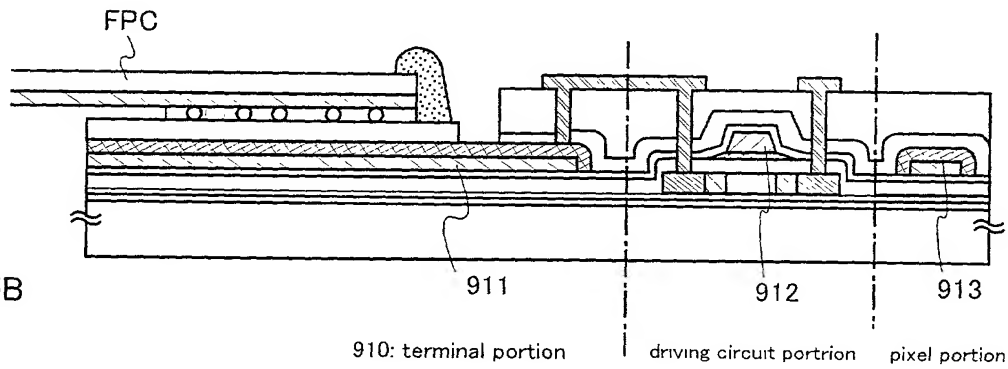


Fig. 15B

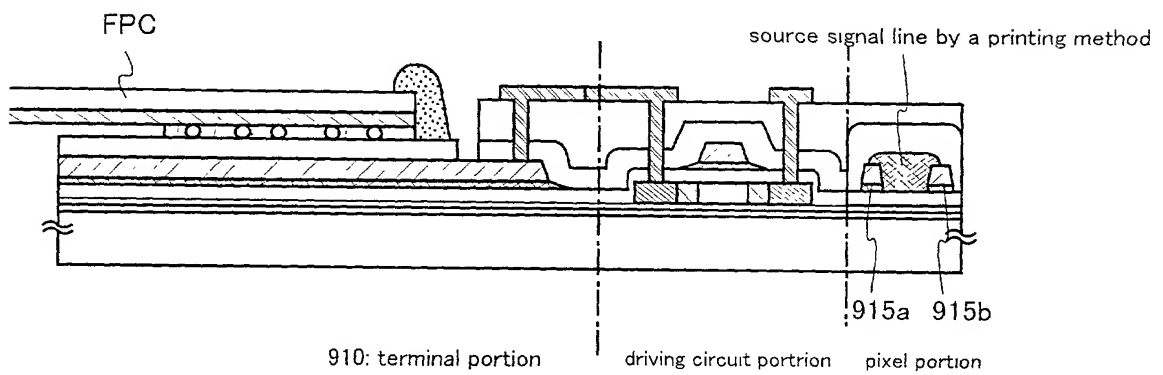


Fig. 15C

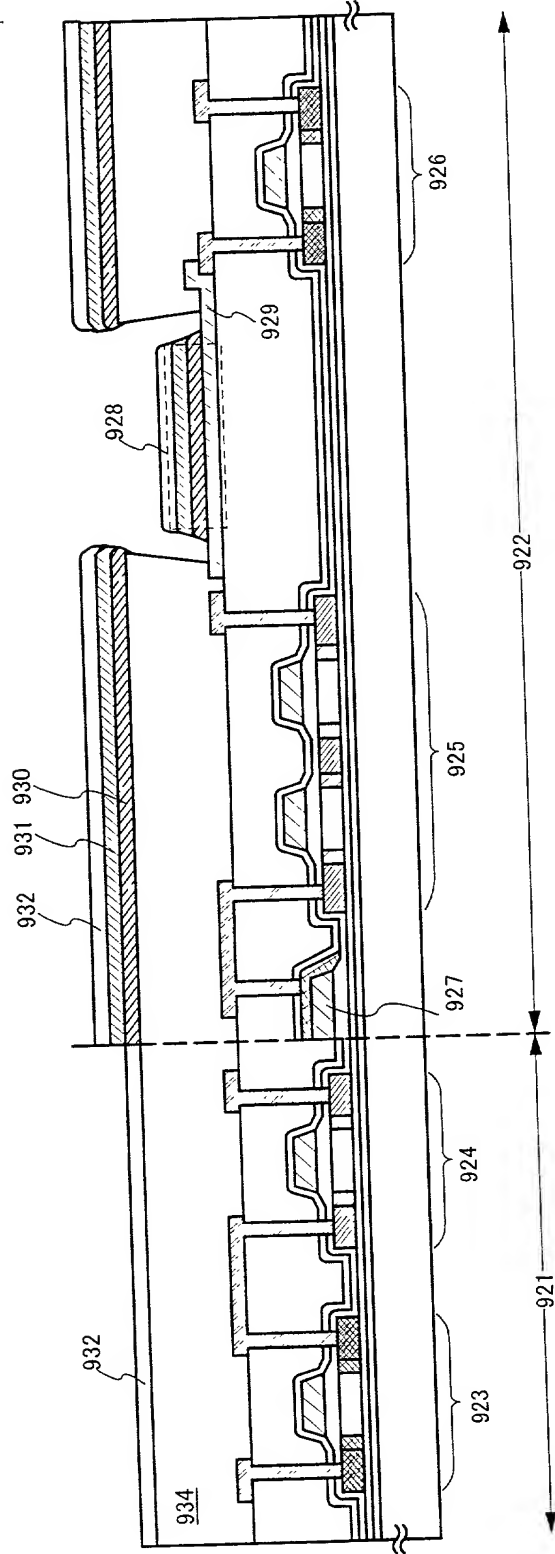


Fig. 16

Footnote 2922001

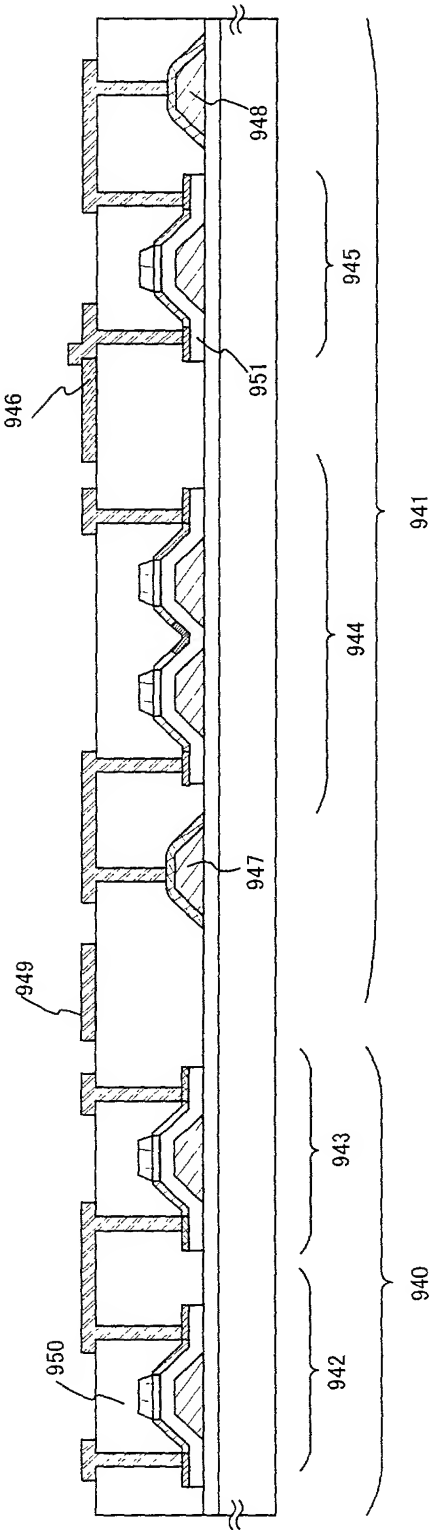


Fig. 17



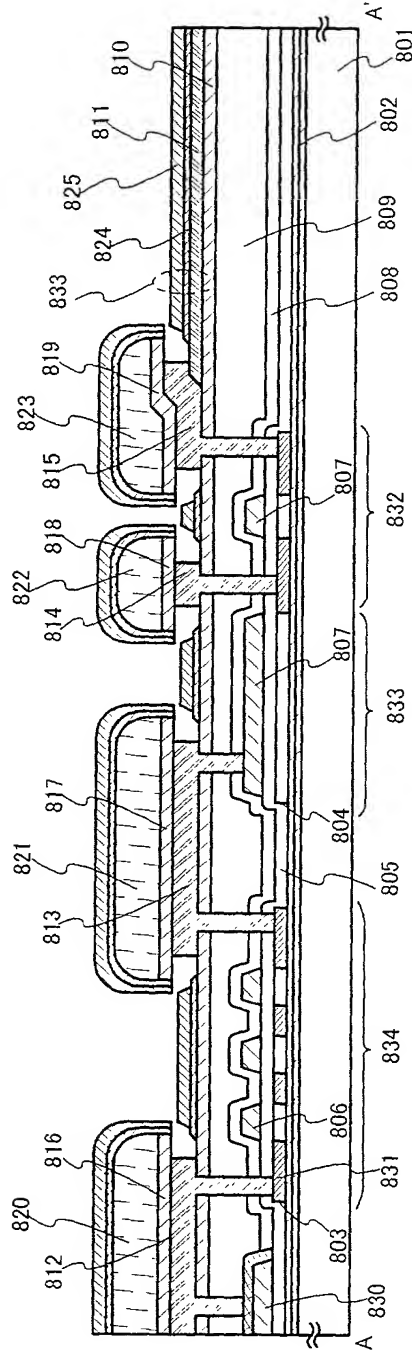


Fig. 18

FIG. 18

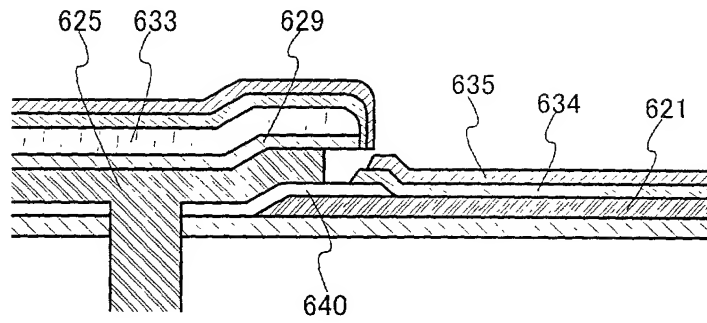


Fig. 19

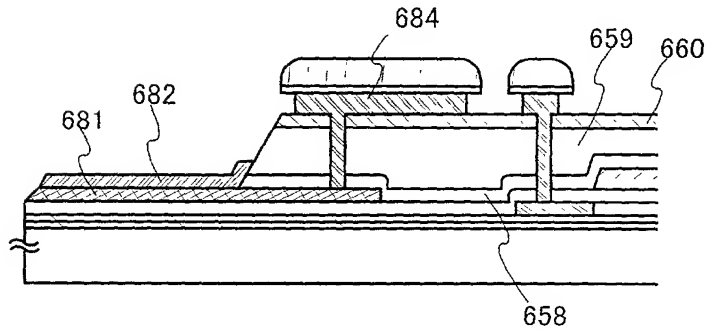


Fig. 20A

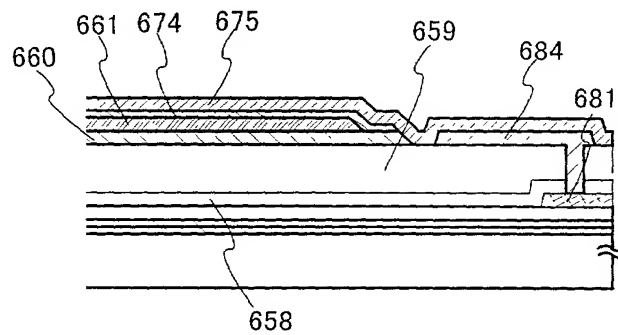


Fig. 20B

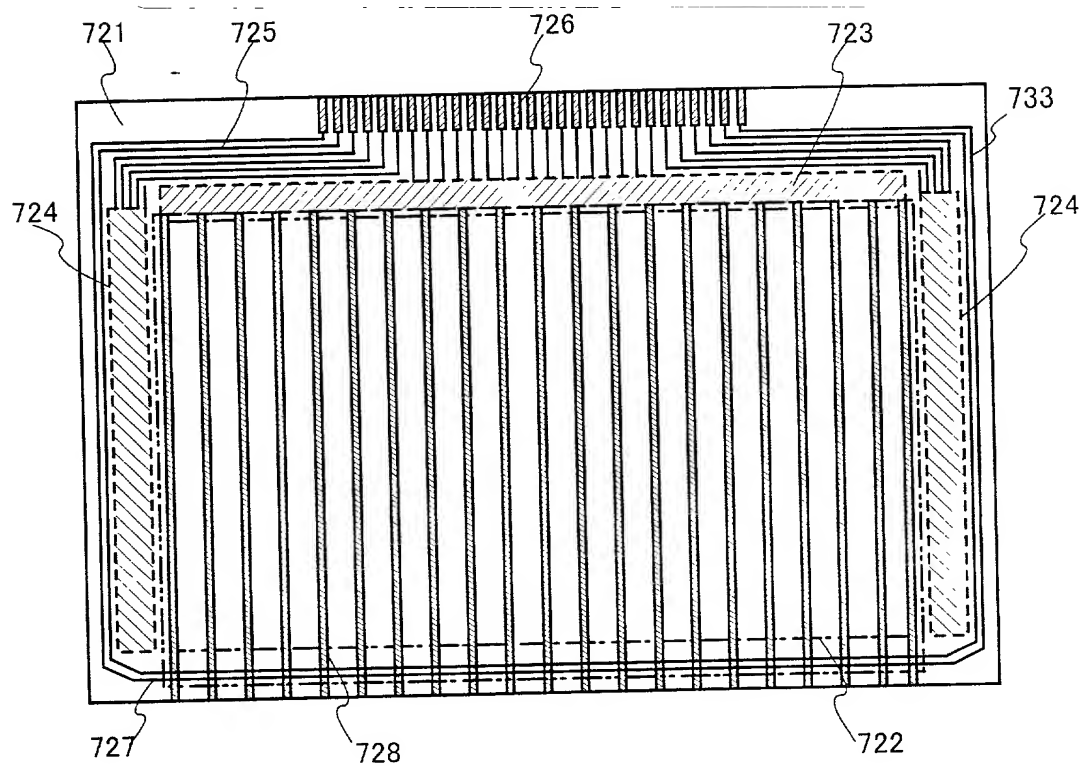


Fig. 21

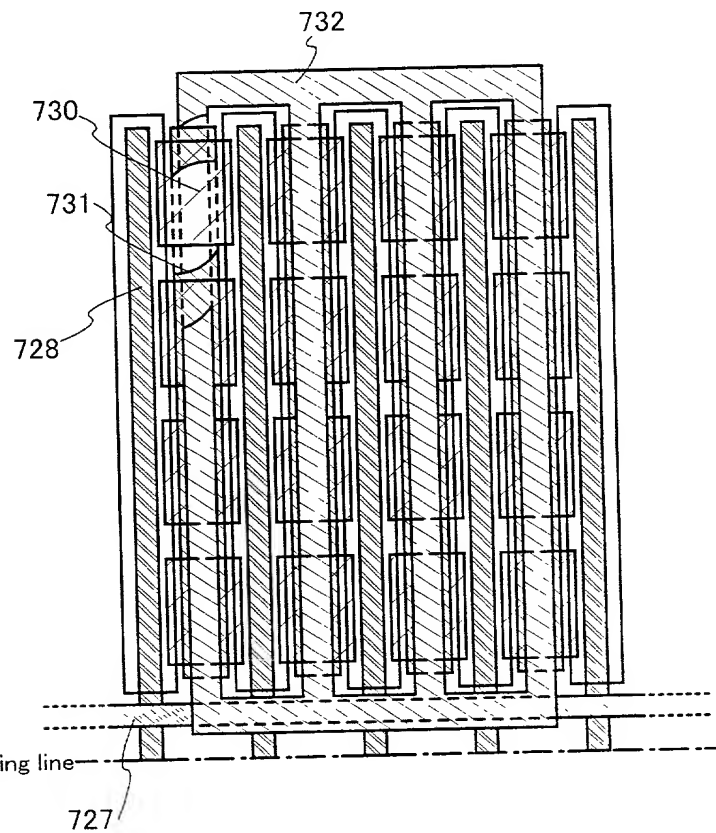


Fig. 22

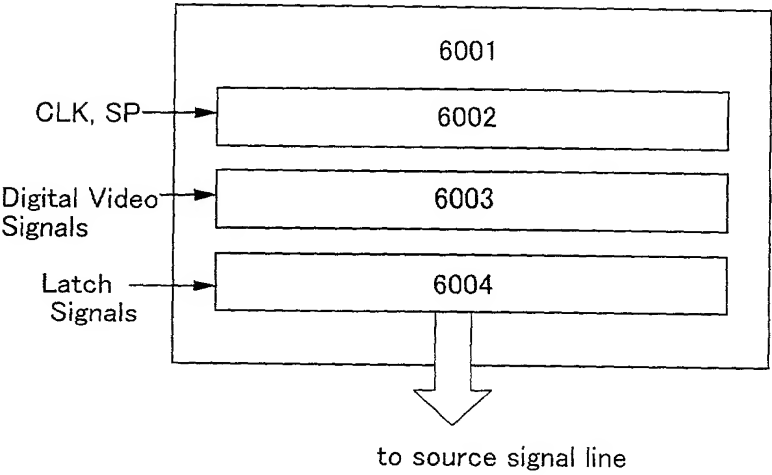


Fig. 23A

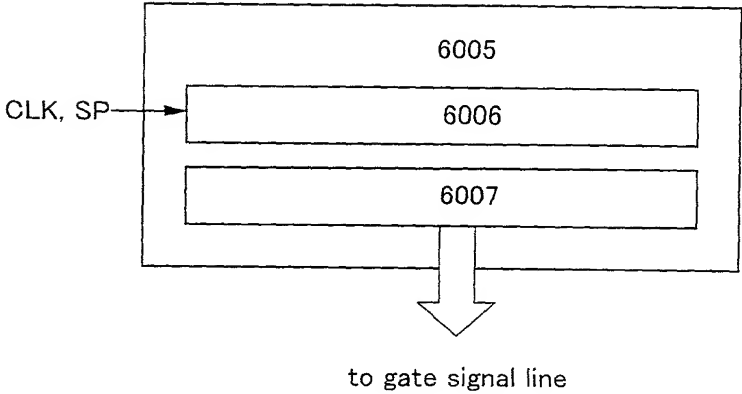


Fig. 23B

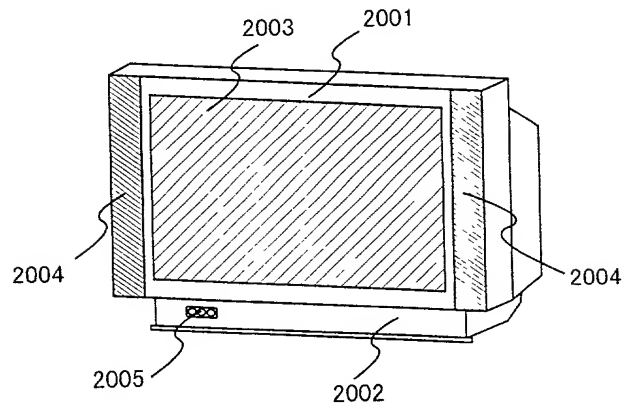


Fig. 24A

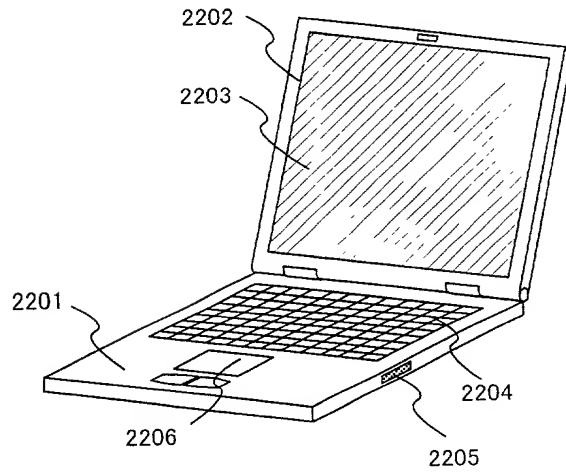


Fig. 24B

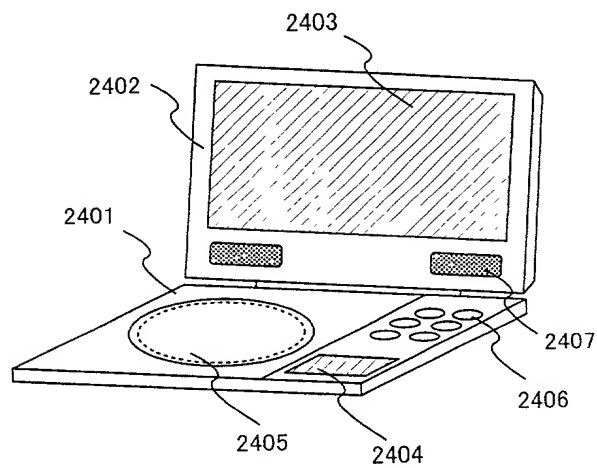


Fig. 24C

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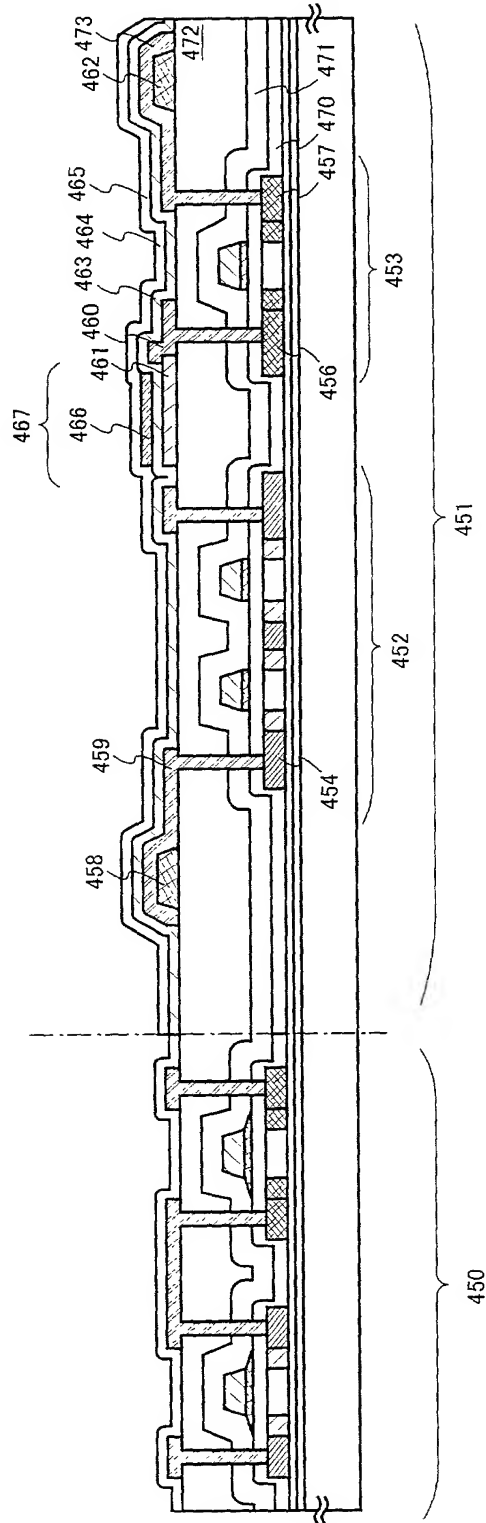


Fig. 25